

JCS/705

JCS/705
U.S. PRO
09/866269
05/25/01

	Subclass	
Class		ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. <i>lsp</i> SCANNED	PATENT DATE <i>AS4 Q.A. C/T</i>
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APPLICATION NO. 09/866269	CONT/PRIOR	CLASS 438 <i>327</i>	SUBCLASS <i>274</i>	ART UNIT 2812 <i>2816</i>	EXAMINER <i>Coy</i>
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APPLICANTS
Sasan Cyrusian

TITLE
Differential CMOS controlled delay unit

PTO-2040
12/99

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ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____ _____					
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	(Primary Examiner) _____ (Date) (Legal Instruments Examiner) _____ (Date)			ISSUE FEE	
				Amount Due	
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